

# Transactions on High-Performance Embedded Architectures and Compilers

Lecture Notes in  
Computer Science

LNCS

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## Scope

The new LNCS Transactions on HiPEAC aims at timely dissemination of research contributions in computer architecture and compilation methods for high-performance and embedded computer systems.

Recognizing the convergence of embedded and general-purpose computer systems, this LNCS Transactions publishes original research on systems targeted at specific computing tasks as well as systems with broad application bases.

The scope of this publication therefore covers all aspects of computer architecture and code generation and compiler optimization methods which are of interest to researchers and practitioners designing future embedded systems.

## Issues & Submissions

The LNCS Transactions on HiPEAC will have four issues per year of which about half of them focus on specific themes or topic areas – *special issues*. Prospective guest editors are welcome to propose themes for special issues.

The review process is managed by an editorial board of leading experts in the areas covered by the journal.

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