

# 11th International Workshop on Software and Compilers for Embedded Systems (SCOPES)

March 13-14, 2008

ICM, Munich, Germany

## March 13<sup>th</sup>

18:30 Pre-Workshop Dinner at Bierstüberl, ICM, Munich;  
free for registered workshop attendees

## March 14<sup>th</sup>

08.30 - 08.35 **Welcome and introduction**

08.35 - 10.00 **Session 1: Optimization and code generation for embedded processors**  
Session Chair: Hans van Someren

08.35 - 09.00 Xiaoyan Jia, Jie Guo, GerhardFettweis:  
*Integrated Code Generation by Using Fuzzy Control System*

09.00 - 09.30 Mattias Eriksson, Oskar Skoog, Christoph Kessler:  
*Optimal vs. Heuristic Integrated Code Generation for Clustered VLIW Architectures*

09.30 - 10.00 Paul Lokuciejewski, Heiko Falk, Peter Marwedel, Henrik Theiling:  
*WCET-Driven, Code-Size Critical Procedure Cloning*

10.00 - 10.30 **Break**

10.30 - 11.45 **Session 2: Keynote talk**

Koen De Bosschere:  
*Memory footprint reduction for embedded systems*

11.45 - 12.00 Discussion

12.00 - 13.00 **Lunch**

13.00 - 14.30 **Session 3: Memory- and addressing-aware compilation**  
Session Chair: Koen De Bosschere

13.00 - 13.30 Tjerk Bijlsma, Marco Bekooij, Pierre Jansen, Gerard Smit:  
*Communication between Nested Loop Programs via Circular Buffers in an Embedded Multiprocessor System*

13.30 - 14.00 Alastair Murray, Björn Franke:  
*Fast Source-Level Data Assignment to Dual Memory Banks*

14.00 - 14.30 Hesham Ali, Hatem El-Boghdadi, Samir Shaheen:  
*A New Heuristic for SOA Problem Based on Effective Tie Break Function*

14.30 - 15.00 **Break**

15.00 - 16.00 **Session 4: Validation and simulation**

15.00 - 15.30 Hugo Venturini, Frederic Riss, Jean-Claude Fernandez, Miguel Santana:  
*A Fully-Non-Transparent Approach To The Code Location Problem*

15.30 - 16.00 Björn Franke:  
*Fast Cycle-Approximate Instruction Set Simulation*

16.00 - 16.20 Discussion

16.20 - 16.30 **Closing**