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**IFIP/IEEE VLSI-SoC 2008 - International
Conference on Very Large Scale Integration**

October 13-15, 2008, Rhodes Island, Greece

VLSI-SoC 2008 is the 16th in a series of international conferences sponsored by IFIP TC 10 Working Group 10.5 and IEEE CEDA that explores the state-of-the-art and the new developments in the field of Very Large Scale Integration Systems and their designs. Previous Conferences have taken place in Edinburgh, Trondheim, Tokyo, Vancouver, Munich, Grenoble, Gramado, Lisbon, Montpellier, Darmstadt, Perth, Nice and Atlanta. The purpose of the Conference is to provide a forum to exchange ideas and to show industrial and research results in the fields of VLSI/ULSI Systems, VLSI CAD and Microelectronic Design and Test.

- Topics of interest include but are not limited to:
- Analog, Digital, and Mixed-Signal IC Design
- 3-D Integration and Physical Design
- Deep Submicron Design and Modeling Issues
- New Devices and MEMS
- Testability and Design for Test
- CAD and Tools
- Digital Signal Processing and Image Processing IC Design
- Prototyping, Validation, and Verification
- Modeling and Simulation
- System-On-Chip Design
- Embedded Systems Design
- New Architectures and Compilers
- Reconfigurable Systems
- Low-Power Design
- Logic and High-Level Synthesis
- New Applications (communications, biosystems, video, automobile, security, sensor networks, etc.)
- Real-Time Systems

The proceedings will be published by IFIP (with ISBN) and available through IEEE *Xplore*. They will be distributed during the conference to all participants. A selection of the conference best papers will be invited to submit an extended version to be included as chapters of a book to be published by Springer.



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<http://vlsi.ee.duth.gr/vlsisoc-2008>